Functional Lower Bounds for Restricted Arithmetic **Circuits of Depth Four**

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Abstract

Recently, Forbes, Kumar and Saptharishi [CCC, 2016] proved that there exists an explicit $d^{O(1)}$ variate and degree d polynomial $P_d \in \mathsf{VNP}$ such that if any depth four circuit C of bounded formal degree d which computes a polynomial of bounded individual degree O(1), that is functionally equivalent to P_d , then C must have size $2^{\Omega(\sqrt{d}\log d)}$.

The motivation for their work comes from Boolean Circuit Complexity. Based on a characterization for ACC⁰ circuits by Yao [FOCS, 1985] and Beigel and Tarui [CC, 1994], Forbes, Kumar and Saptharishi [CCC, 2016] observed that functions in ACC^0 can also be computed by algebraic $\Sigma \wedge \Sigma \Pi$ circuits (i.e., circuits of the form – sums of powers of polynomials) of $2^{\log^{O(1)} n}$ size. Thus they argued that a $2^{\omega(\operatorname{poly}\log n)}$ "functional" lower bound for an explicit polynomial Q against $\Sigma \wedge \Sigma \Pi$ circuits would imply a lower bound for the "corresponding Boolean function" of Q against non-uniform ACC⁰. In their work, they ask if their lower bound be extended to $\Sigma \wedge \Sigma \Pi$ circuits.

In this paper, for large integers n and d such that $\omega(\log^2 n) \leq d \leq n^{0.01}$, we show that any $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most $O\left(\frac{d}{k^2}\right)$ that functionally computes Iterated Matrix Multiplication polynomial $\mathsf{IMM}_{n,d} \ (\in \mathsf{VP})$ over $\{0,1\}^{n^2d}$ must have size $n^{\Omega(k)}$. Since Iterated Matrix Multiplication $\mathsf{IMM}_{n,d}$ over $\{0,1\}^{n^2d}$ is functionally in GapL , improvement of the afore mentioned lower bound to hold for quasipolynomially large values of individual degree would imply a fine-grained separation of ACC⁰ from GapL.

For the sake of completeness, we also show a syntactic size lower bound against any $\Sigma \wedge \Sigma \Pi$ circuit computing $\mathsf{IMM}_{n,d}$ (for the same regime of d) which is tight over large fields. Like Forbes, Kumar and Saptharishi [CCC, 2016], we too prove lower bounds against circuits of bounded formal degree which functionally compute $\mathsf{IMM}_{n,d}$, for a slightly larger range of individual degree.

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1 Introduction

Owing to the difficulty in proving Boolean circuit size lower bounds, Valiant proposed that we prove lower bounds in an "algebraic setting" as the underlying algebraic structure could help us understand the computations better. Valiant further conjectured that any circuit theoretic proof for $P \neq NP$ would have to be preceded by an analogous result in this more constrained arithmetic model [42].

Arithmetic circuits (also called as algebraic circuits) are directed acyclic graphs such that the leaf nodes are labeled by variables or constants from the underlying field, and every non-leaf node is labeled either by $a + or \times$. Every node computes a polynomial by operating on its inputs with the operation given by its label. The computation flows from the leaves to the output node. Complexity of computation here is quantified by the size of the circuit, which is the number of nodes in it.

It is conjectured that Permanent polynomial does not have polynomial size arithmetic circuits [41]. Bürgisser [6] showed that if Permanent polynomial were to have a polynomial sized arithmetic circuit then this would imply $\#P \subseteq \mathsf{FNC}^3/\mathsf{poly}$ which would further imply that $\mathsf{NP} \subseteq \mathsf{P}/\mathsf{poly}$ which leads to (1) $\mathsf{PH} \subseteq \Sigma_p^2$ [20] and (2) $\mathsf{AM} = \mathsf{MA}$ [3], both of which go against widely believed conjectures. Thus, a central question in the field of algebraic complexity theory is to show that Permanent polynomial (or any closely related polynomial of interest) needs superpolynomial sized arithmetic circuits to compute it.

Four decades after the problem was formulated, the best known size lower bound is still super linear [4]. Over the span of last three decades, researchers have considered restricted arithmetic circuits and here we have seen a great progress towards proving lower bounds under these restrictions (see [40, 38] for a detailed survey). In a surprising result, Agrawal and Vinay [1] showed that it is sufficient to prove subexponential size lower bounds against depth four circuits, to prove super polynomial size lower bounds against general arithmetic circuits.

A depth four circuit¹ (denoted by $\Sigma\Pi\Sigma\Pi$) computes polynomials that can also be expressed as a sum of products of polynomials.

$$P(X) = \sum_{i} \prod_{j} Q_{i,j} .$$

Syntactic lower bounds

We say that a polynomial P has a syntactic circuit size lower bound of s against class C of circuits if no circuit in C of size strictly smaller than s syntactically computes P.

Strong syntactic size lower bounds for depth four circuits were proven in restricted settings: Bounded fan-in [17, 24, 15, 12, 27], Homogeneous [21, 26, 22, 28], Multilinear [36, 11], and Multi-r-ic [25, 8, 7]. In a breakthrough, Limaye, Srinivasan and Tavenas recently proved superpolynomial size lower bounds against all constant depth circuits [29]. Prior to that the best known lower bound for depth four circuits was super-quadratic [19] (which improves upon super-linear lower bounds due to Shoup and Smolensky [39] and Raz [34]).

¹ Generally speaking, a depth four circuit can also be of the form $\Pi\Sigma\Pi\Sigma$ but we follow the convention that the root node is a + node. Under such a convention $\Pi\Sigma\Pi\Sigma$ circuit is a depth five circuit.

Functional lower bounds

For a set $B \subseteq \mathbb{F}$, we say that two polynomials $P(x_1, \ldots, x_N)$ and $Q(x_1, \ldots, x_N)$ are functionally equivalent over B^N if $P(\mathbf{a}) = Q(\mathbf{a})$ for all $\mathbf{a} \in B^N$. We say that a circuit C functionally computes a polynomial $P \in \mathbb{F}[x_1, \ldots, x_N]$ over B^N if the output polynomial $f \in F[x_1, \ldots, x_N]$ of C is functionally equivalent to P over B^N .

We say that a polynomial P has a functional size lower bound of s against a class C of circuits if no polynomial that is computed by circuits in C of size strictly less than s, is functionally equivalent to P over B^n for some $B \subseteq \mathbb{F}$.

Forbes, Kumar and Saptharishi [14] proved exponential functional lower bounds for a polynomial in VNP against depth four circuits of bounded formal degree and bounded individual degree O(1). Formally, they showed that there is an explicit polynomial P_d of degree d over $\approx d^3$ variables such that no depth four circuit of bounded formal degree d and size smaller than $2^{c(\sqrt{d}\log d)}$ (for a small constant c) that computes a polynomial of bounded individual degree at most O(1) can be functionally equivalent to P_d . Apart from this work, strong functional lower bounds are known against depth three circuits over finite fields [16], multilinear formulas [33, 32, 35, 36, 10, 11], and set-multilinear formulas [31, 29].

The motivation for the work of [14] comes from Boolean circuit complexity. ACC⁰ circuits are constant depth Boolean circuits that have AND, OR, NOT and MOD gates. Allender and Gore [2] showed that *uniform* ACC⁰ circuits of subexponential size cannot compute Permanent. In a major breakthrough, Williams [44] showed that there exists a function in NEXP such that it cannot be computed by polynomial sized *nonuniform* ACC⁰ circuits. Recently Murray and Williams [30] further improved the situation to show that there exists a function in NQP such that it needs superpolynomial size ACC⁰ circuits to compute it.

Beigel and Tarui [5] showed that every language L in the class ACC^0 can be recognized by a family of depth two^2 deterministic circuits with a symmetric function gate at the root and $2^{\log^{O(1)} n}$ many AND gates of fan-in $\log^{O(1)} n$ in the second layer. Over large fields, Forbes, Kumar and Saptharishi [14] observed that given this Boolean circuit, there is an algebraic circuit of depth four which computes polynomials of the form – sum of $2^{\log^{O(1)} n}$ many powers of polynomials each of whose monomials are supported on at most $\log^{O(1)} n$ many variables such that outputs of both of these circuits are functionally equivalent.

 $\Sigma \wedge \Sigma \Pi$ circuits are depth four circuits that compute polynomials which can be expressed as sums of powers of polynomials. $\Sigma \wedge \Sigma \Pi^{[t]}$ circuits are depth four circuits that compute polynomials which can be expressed as sums of powers of polynomials each of whose monomials are supported on at most t many variables.

We can summarize the afore mentioned discussion formally as follows.

- ▶ Lemma 1 (Lemma 3.2, [14]). Let \mathbb{F} be any field of characteristic zero or at least $\exp(\omega(\operatorname{poly}(\log n)))$. If a function $f:\{0,1\}^n \mapsto \{0,1\}$ is in ACC^0 then there exists a polynomial $P_f \in \mathbb{F}[x_1,\ldots,x_n]$ such that
- \blacksquare P_f and f are functionally equivalent over $\{0,1\}^n$, and
- P_f can be computed by a $\Sigma \wedge \Sigma \Pi$ circuit of top fan-in at most $2^{\log^{O(1)} n}$ and bottom support at most $\log^{O(1)} n$.

² Here the variables can appear negated at the leaves that feed into the AND gates. Even though it is stated as depth two in the paper, the longest leaf to root path in this circuit is of length 3. Leaf node \rightarrow AND \rightarrow root.

Thus, to show a lower bound against ACC^0 circuits in the Boolean setting, it is sufficient to show a functional lower bound of $\exp(\omega(\mathsf{poly}(\log n)))$ for a polynomial P would imply that the Boolean part³ of P is not in ACC^0 .

▶ Lemma 2 (Lemma 3.3, [14]). Let \mathbb{F} be any field of characteristic zero or at least $\exp(\omega(\operatorname{poly}(\log n)))$. Then $a \exp(\omega(\operatorname{poly}(\log n)))$ functional size lower bound for a $n^{O(1)}$ -variate and $n^{O(1)}$ degree polynomial $P \in \mathbb{F}[X]$ against $\Sigma \wedge \Sigma \Pi^{[\operatorname{poly}(\log(n))]}$ circuits over \mathbb{F} would imply that Boolean part of P is not in ACC^0 .

Forbes, Kumar and Saptharishi [14] through an open question in their paper ask if such functional lower bounds can also be proved for $\Sigma \wedge \Sigma \Pi$ circuits. We in this paper show strong functional lower bounds against all $\Sigma \wedge \Sigma \Pi$ circuits which output polynomials of bounded individual degree.

A circuit C is said to have a bounded individual degree⁴ r if the polynomial output by the circuit C has degree at most r with respect to each of its variables.

▶ Theorem 3 (Functional Lower Bounds for $\Sigma \wedge \Sigma \Pi$ circuits of Bounded Individual Degree). Let n be a large integer. Let d, k and r be such that $\omega(\log^2 n) \leq d \leq n^{0.01}$ and $r \leq \frac{d}{1201k^2}$. Any depth four $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree r computing a function equivalent to $\mathrm{IMM}_{n,d}$ on $\{0,1\}^{n^2d}$, must have size at least $n^{\Omega(k)}$.

Note that there is a trade-off between the lower bound on the circuit size and the upper bound on the range of r this lower bound can be achieved for.

Since Iterated Matrix Multiplication $\mathsf{IMM}_{n,d}$ over $\{0,1\}^{n^2d}$ is functionally⁵ in GapL [43, Section 6], improvement of the afore mentioned lower bound to hold for quasipolynomially large values of individual degree would imply a fine-grained separation of ACC^0 from GapL .

By a divide and conquer construction, we get a depth four $\Sigma\Pi\Sigma\Pi$ circuit of size $n^{O(\sqrt{d})}$ that computes $\mathsf{IMM}_{n,d}$ such that the fan-in of both the product gates is equal to \sqrt{d} . Using the identity

$$m! \cdot x_1 x_2 \dots x_m = \sum_{S \subseteq [m]} \left(\sum_{i \in S} x_i \right)^m \cdot (-1)^{m-|S|}$$

(attributed to Fischer [13] and Ryser [37] in [18]), over large fields this circuit can be converted into a $\Sigma \wedge \Sigma \Pi$ circuit of size $n^{O(\sqrt{d})}$. We will now show a lower bound of $n^{\Omega(\sqrt{d})}$ for $\mathsf{IMM}_{n,d}$ against any $\Sigma \wedge \Sigma \Pi$ circuits. From the afore mentioned discussion, this lower bound is optimal up to a constant in the exponent over large fields.

▶ Theorem 4 (Syntactic Lower Bounds for $\Sigma \wedge \Sigma \Pi$ circuits). Let n and d be a large integers such that $\omega(\log^2 n) \leq d \leq n^{0.01}$. Any depth four $\Sigma \wedge \Sigma \Pi$ circuit computing $\mathsf{IMM}_{n,d}$ must have size at least $n^{\Omega(\sqrt{d})}$.

³ Bürgisser [6] defined the boolean part of a polynomial $P(x_1, \ldots, x_n)$ (denoted by BP(P)) to be a function that agrees with P over all evaluations over $\{0, 1\}^n$.

⁴ Not to be confused with the multi-r-ic circuits dealt with in [23, 25, 8, 7].
⁵ Bürgisser [6] showed that boolean part of any polynomial in VP lies in FNC^3 /poly, and in particular $IMM_{n,d} \in VP$. On the other hand, Vinay [43] identified that this problem of computing Iterated Matrix Product of integer matrices (denoted by ITMATPROD) is in fact in the class GapL which consists of all problems that are logspace reducible to determinant computation of an integer matrix. This is a better characterization as $GapL \subseteq NC^2 \subseteq FNC^3$ /poly.

Proof of this lemma can be found in [9, Section 5]. Recall that Forbes, Kumar and Saptharishi [14] proved functional lower bounds for a polynomial in VNP against depth four circuits of bounded formal degree whose output polynomials are of bounded individual degree O(1). Here shall prove functional lower bounds for a polynomial in VP against depth four circuits of bounded formal degree whose output polynomials are of bounded individual degree $O(\log n)$.

Formal degree of a circuit is the maximum degree of any polynomial that could be computed by this circuit structure sans the constants nor cancellations. Formal degree of a circuit is inductively defined as follows: for a leaf node w, the formal degree 1 if it is labeled by a variable and 0 otherwise. Formal degree of a sum node is the maximum over all the formal degrees of its children, and formal degree of a product node is equal to the sum over all the formal degrees of its children.

▶ Theorem 5 (Functional Lower Bounds for $\Sigma\Pi\Sigma\Pi$ Circuits of Bounded Formal Degree). Let n, d and r be integers such that $\omega(\log^2 n) \leq d \leq n^{0.01}$ and $r \leq \frac{\log n}{12}$. Any depth four $\Sigma\Pi\Sigma\Pi$ circuit of formal degree d and bounded individual degree r that computes a function equivalent to $\mathrm{IMM}_{n,d}$ on $\{0,1\}^{n^2d}$, must have size at least $n^{\Omega\left(\sqrt{\frac{d}{r}}\right)}$.

Proof of this lemma can be found in [9, Section 6]. We would to remark that the afore mentioned bound and the bound for similar circuits in [14] can be made to work for formal degree that is slightly larger than d.

Related Work

For the sake of brevity, we shall denote the $\Sigma \wedge \Sigma \Pi$ circuits of bounded individual degree r by $(\Sigma \Pi \Sigma \Pi)^{\leq r}$. We in this table summarize our results in comparison to the work of [14].

Circuit model	Work	Hard multilinear polynomial family	Lower Bound	Range of parameters
$(\Sigma\Pi\Sigma\Pi)^{\leq r}$ & formal degree d	[14]	Nisan-Wigderson polynomial $NW_{m,d} \in VNP$ with md many variables and degree d	$2^{\Omega\left(\sqrt{d}\log{(md)}\right)}$	$m = \Theta(d^2)$, and $r \le O(1)$.
$(\Sigma\Pi\Sigma\Pi)^{\leq r}$ & formal degree d	This work	Iterated Matrix Multiplication polynomial $IMM_{n,d} \in VP$ with n^2d many variables and degree d	$n^{\Omega\left(\sqrt{\frac{d}{r}}\right)}$	$\omega(\log^2 n) \le d \le n^{0.01},$ and $r \le \frac{\log n}{12}.$
$(\Sigma \wedge \Sigma \Pi)^{\leq r}$	This work	$IMM_{n,d}$	$n^{\Omega(k)}$	$\omega(\log^2 n) \le d \le n^{0.01},$ and $r \le \frac{d}{1201k^2}.$

Our work is inspired by [14]'s line of research and depends on the techniques introduced by them. We take their research a bit further.

Complexity measure and proof overview

Let the variable set X be partitioned into two fixed, disjoint sets Y and Z. Let σ_Y : $\mathbb{F}[Y \sqcup Z] \mapsto \mathbb{F}[Z]$ be a linear map such that for any polynomial P(Y,Z), $\sigma_Y(P) \in \mathbb{F}[Z]$ is obtained by setting every variable from Y to zero and leaving the variables from Z untouched.

For a polynomial $P(x_1, \ldots, x_N)$, let $\operatorname{mult}(P)$ be defined to be equal to $P \operatorname{mod} \{(x_i^2 - x_i) \mid i \in [N]\}$. Similarly, let $\operatorname{mult}(V)$ for a subspace V of polynomials in $\subseteq \mathbb{F}[x_1, \ldots, x_N]$, be defined as follows.

$$\operatorname{mult}(V) = {\operatorname{mult}(P) \mid P \in V}.$$

For a polynomial P(Y,Z) and a set $S \subseteq \mathbb{F}$, let $\operatorname{Eval}_{S}^{[Y \cup Z]}(P)$ denote the vector of evaluations of polynomial P over $S^{|Y \cup Z|}$ as follows.

$$\operatorname{Eval}_{S}^{[Y \cup Z]}(P(Y, Z)) = (P(\mathbf{a}))_{\mathbf{a} \in S^{|Y \cup Z|}}.$$

This definition can be extended to a set V of polynomials over $\mathbb{F}[Y \cup Z]$ as follows.

$$\mathrm{Eval}_S^{[Y \cup Z]}(V) = \left\{ \mathrm{Eval}_S^{[Y \cup Z]}(P(Y,Z)) \mid P(Y,Z) \in V \right\} \,.$$

We use $\partial_{\overline{Y}}^{\leq k}P$ to denote the set of all partial derivatives of P of order at most k with respect to monomials over variables just from Y, and $Z^{=\ell} \cdot \sigma_Y(\partial_{\overline{Y}}^{=k}P)$ to refer to the set of polynomials obtained by multiplying each polynomial in $\sigma_Y(\partial_{\overline{Y}}^{\leq k}P)$ with monomials of degree equal to ℓ in Z variables.

Main measure – Multilinear Shifted Evaluation Dimension $(\mathbf{mSED}_{k,\ell}^{[Y,Z]})$

Forbes, Kumar and Saptharishi [14] defined Shifted Evaluation Dimension which counts the dimension of space of vectors each of which is a list of evaluations of polynomials $\{0,1\}^{|X|}$ where these polynomials are Z-shifts of partial evaluations.

$$\operatorname{SED}_{k,\ell}^{[Y,Z]}(P(Y,Z)) = \dim\left(\operatorname{Eval}_{\{0,1\}^{|Z|}}\left\{Z^{=\ell} \cdot \mathbb{F}\operatorname{-span}\left\{P(\mathbf{a},Z) \mid \mathbf{a} \in \{0,1\}_{\leq k}^{|Y|}\right\}\right\}\right)$$

We just make a minor modification to this measure to better relate our measure with the measure of Projected Shifted Skew Partial derivatives ([8, 7]) and this helps us obtain bounds that we could not get before.

$$\mathrm{mSED}_{k,\ell}^{[Y,Z]}(P(Y\!,Z\!)) = \mathrm{dim}\left(\mathrm{Eval}_{\{0,1\}^{|Z|}}\left\{\mathrm{mult}\left(Z^{=\ell}\cdot\mathbb{F}\text{-span}\left\{P(\mathbf{a},Z)\mid \mathbf{a}\in\{0,1\}_{\leq k}^{|Y|}\right\}\right)\right\}\right)$$

In spirit, it is still the measure of [14] and thus we do not consider this to be a new measure. We just make a minor modification to relate this measure with their measure of Projected Shifted Skew Partial derivatives ([8, 7]) and this helps us obtain bounds that we could not get before.

By unfurling the above definition, we can see that if two N-variate polynomials $P_1(Y, Z)$ and $P_2(Y, Z)$ (defined on the same variable sets) are functionally equivalent over $\{0,1\}^N$ then $\mathrm{mSED}_{k,\ell}^{[Y,Z]}(P_1(Y,Z)) = \mathrm{mSED}_{k,\ell}^{[Y,Z]}(P_2(Y,Z))$. Note that two polynomials which are not functionally equivalent over \mathbb{F}^N can end up being functionally equivalent over $\{0,1\}^N$ but to show that two polynomials are not functionally equivalent, it is sufficient to show that they are not functionally equivalent over $\{0,1\}^N$.

The crux of our work henceforth is to show that the polynomial of interest, $\mathsf{IMM}_{n,d}$ is not functionally equivalent over $\{0,1\}^{n^2d}$ to the polynomials that are output by the $\Sigma \wedge \Sigma \Pi$ circuits of bounded individual degree. That is, we need to show that $\mathsf{mSED}_{k,\ell}^{[Y,Z]}(\mathsf{IMM}_{n,d}(Y,Z))$ is much larger than $\mathsf{mSED}_{k,\ell}^{[Y,Z]}(C(Y,Z))$ where C is a $\Sigma \wedge \Sigma \Pi$ circuit of small size and bounded individual degree.

Though two N-variate polynomials P_1 and P_2 that are functionally equivalent over $\{0,1\}^N$ have the same (multilinear) shifted evaluation dimension, the dimension of their partial derivative spaces can be very different (see [14, Section 1.2.1] for an example). However in

certain special cases Forbes, Kumar and Saptharishi [14] do manage to relate the shifted evaluation dimension, and a partial derivate based measure well enough for their proof to work. We shall do something very similar.

Let C be a $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most r that computes a polynomial that is functionally equivalent to a homogeneous and degree d set-multilinear polynomial P(X) defined over the sets $X = X_1 \sqcup \ldots \sqcup X_d$ such that $Y = X_{i_1} \sqcup \ldots X_{i_k}$ (for a fixed subset $\{i_1, \ldots, i_k\} \subseteq [d]$) and $Z = X \setminus Y$. Similar to [14], we show that we can bound the multilinear shifted evaluation dimension on the above and below by an auxiliary measure that counts the dimension of a space of a specially chosen syntactic polynomials. For every value of k, ℓ and r, we can show that

$$\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(P(Y,Z)) \leq \mathrm{mSED}_{k,\ell}^{[Y,Z]}(P(Y,Z)) = \mathrm{mSED}_{k,\ell}^{[Y,Z]}(C(Y,Z)) \leq \mathrm{PSSPD}_{rk,\ell}^{[Y,Z]}(C(Y,Z)) \,.$$

Upon instantiating the above expression with explicit homogeneous and set-multilinear polynomial $\mathsf{IMM}_{n,d}(Y,Z)$, and if for a suitable setting of values of k,ℓ and r, we get that $\mathsf{PSSPD}^{[Y,Z]}_{k,\ell}(\mathsf{IMM}_{n,d}(Y,Z))$ is much larger than $\mathsf{PSSPD}^{[Y,Z]}_{rk,\ell}(C(Y,Z))$ where C is a $\Sigma \wedge \Sigma \Pi$ circuit that computes polynomials of bounded individual degree r of size s, then we can infer that $\mathsf{IMM}_{n,d}(Y,Z)$ cannot be functionally computed by this class of circuits, thus giving us a functional size lower bound of s for this explicit polynomial.

Auxiliary measure – Projected Skew Shifted Partial Derivatives $(PSSPD_{k,\ell}^{[Y,Z]})$

The following is a measure⁶ borrowed from [7] which was used to prove syntactic lower bounds for multi-r-ic depth four circuits.

$$\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(P(Y,Z)) = \dim \left(\mathbb{F}\text{-span}\left\{ \mathrm{mult}\left(Z^{=\ell} \cdot \sigma_Y\left(\partial_Y^{\leq k}P\right)\right) \right\} \right) \, .$$

We currently do not know how to directly obtain a bound on $\mathrm{PSSPD}_{rk,\ell}^{[Y,Z]}(C(Y,Z))$ to a value that is much smaller than $\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(\mathrm{IMM}_{n,d}(Y,Z))$. To resolve this issue, we use random restrictions $V \leftarrow D$ to convert our $\Sigma \wedge \Sigma \Pi$ circuit C of size $s \leq n^{\frac{t}{2}}$ that computes a polynomial P of bounded individual degree to a $\Sigma \wedge \Sigma \Pi$ circuit C' of size s and of bottom fan-in at most t that still computes the restricted polynomial P', with a high probability. We can now bound $\mathrm{PSSPD}_{rk,\ell}^{[Y,Z]}(C(Y,Z))$ to a value that is much smaller than $\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}((\mathrm{IMM}_{n,d}(Y,Z))|_V)$. This trick is omnipresent in this line of work [21, 26, 22, 28, 25, 14, 8, 7].

We then borrow the lower bound on $\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(P'(Y,Z))$ (where P' is the polynomial obtained from $\mathrm{IMM}_{n,d}$ after restrictions) from [7].

We would like to remark that $\operatorname{mult}(P)$ for a polynomial $P(x_1,\ldots,x_N)$ was defined to be $P \mod \left\{x_i^2: i \in [N]\right\}$ in $[8,\ 7]$ instead of $P \mod \left\{x_i^2-x_i: i \in [N]\right\}$ as defined here. We use this new definition of mult because $\operatorname{mSED}_{k,\ell}^{[Y,Z]}(P_1(Y,Z))$ may not be equal to $\operatorname{mSED}_{k,\ell}^{[Y,Z]}(P_2(Y,Z))$ under the older definition of $\operatorname{mult}(P) = P \mod \left\{x_i^2: i \in [N]\right\}$ even though $P_1(Y,Z)$ and $P_2(Y,Z)$ are functionally equivalent.

The lower bound on $\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(P'(Y,Z))$ in [7] continues to hold despite this change of definition.

⁶ This measure is an amalgamation of measures – dimension of Projected Shifted Partial derivatives of [21] and dimension of Skew Shifted Partial derivatives of [25].

2 Preliminaries

Notation

- We use [n] to refer to the set $\{1, 2, ..., n\}$.
- For a polynomial f and a monomial m of degree k, we use $\partial_m^k f$ to refer to the kth partial derivate of the polynomial f with respect to the monomial m.
- For a polynomial f, we use $\partial_{Y}^{\leq k}(f)$ to refer to the space of partial derivatives of order at most k of f with respect to monomials of degree at most k in variables from Y.
- We use $Z^{=\ell}$ and $Z^{\leq \ell}$ to refer to the set of all the monomials of degree equal to ℓ and at most ℓ , respectively, in variables Z.
- We use $Z_{ML}^{\leq t}$ to refer to the set of all the multilinear monomials of degree at most t in Z variables.
- For sets A and B of polynomials, we define the product $A \cdot B$ to be the set $\{f \cdot g \mid f \in A \text{ and } g \in B\}.$
- For a monomial m we use Supp(m) to refer to the set of variables that appear in it.
- We use $Z_{\{\leq t\}}$ to refer to the set of all monomials m in Z variables such that $|\operatorname{Supp}(m)| \leq t$.
- ightharpoonup Claim 6. Let $W\subseteq \mathbb{F}[X]$ be a subspace of multilinear polynomials. Then $\dim(W)=\dim(\operatorname{Eval}_{\{0,1\}}^{[X]}(W))$.

Proof. Proof of this claim follows from the facts that every multilinear polynomial in W has a unique evaluation vector, and access to evaluations of a multilinear polynomial over all of $\{0,1\}^{|X|}$ uniquely determines it.

- \blacktriangleright **Proposition 7.** For two sets A and B of polynomials,
- 1. $\operatorname{mult}(A \cdot B) = \operatorname{mult}(\operatorname{mult}(A) \cdot \operatorname{mult}(B))$, and
- 2. $\dim(\operatorname{mult}(\operatorname{mult}(A) \cdot \operatorname{mult}(B))) \leq \dim(\operatorname{mult}(A) \cdot \operatorname{mult}(B)).$

The proof of this proposition easily follows from the fact that mult is a many to one map and not one to many.

- ▶ **Definition 8** (Homogeneous polynomials). A polynomial P of degree d is said to be homogeneous if it can be expressed as a linear combination of just the monomials of degree equal to d.
- ▶ **Definition 9** (Set-multilinear polynomials). A polynomial P is said to be set-multilinear with respect to a set of variables X, under the partition $X = X_1 \sqcup X_2 \sqcup \ldots X_d$ if every monomial m in the monomial support of P is such that $|\text{MonSupp}(m) \cap X_i| \leq 1$ for all $i \in [d]$.
- ▶ **Definition 10** (Multi-r-ic polynomials). A polynomial P is said to be multi-r-ic polynomial if the degree of the polynomial with respect to each of its variables is at most r.

The following lemma (from [17]) is key to the asymptotic estimates required for the lower bound analyses.

▶ **Lemma 11** (Lemma 6, [17]). Let $a(n), f(n), g(n) : \mathbb{Z}_{\geq 0} \to \mathbb{Z}_{\geq 0}$ be integer valued functions such that (f+g) = o(a). Then,

$$\ln\frac{(a+f)!}{(a-g)!} = (f+g)\ln a \pm O\left(\frac{(f+g)^2}{a}\right)$$

We shall now state a few lemmas that help us relate both the complexity measures introduced above.

▶ Lemma 12 (Observation 4.5 in [14]). Let $X = X_1 \sqcup \ldots \sqcup X_d$ and |X| = N. Let $Y = X_1 \sqcup \ldots \sqcup X_k$ for some $k \ll d$. Let P be a homogeneous set multilinear polynomial of degree d with respect to the partition $X_1 \sqcup \ldots \sqcup X_d$. Let $m = Y^e$ be a set multilinear monomial q of degree k over q. Then,

$$\frac{\partial^k P}{\partial Y^{\mathbf{e}}} = P(\mathbf{e}, Z).$$

The following corollary can be obtained from Lemma 12 and proof of this corollary can be found in [9].

▶ Corollary 13 (Similar to Corollary 4.6 in [14]). For a homogeneous and set multilinear polynomial P(Y, Z) which is as defined as in Lemma 12, and for all values of parameters k and ℓ ,

$$\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(P(Y,Z)) \le \mathrm{mSED}_{k,\ell}^{[Y,Z]}(P(Y,Z)).$$

▶ **Lemma 14** (Lemma 4.7 in [14]). Let P(Y,Z) be a multi-r-ic polynomial. Then for every choice of parameters k and ℓ , we have

$$\left\{P(\mathbf{e},Z)\mid \mathbf{e}\in\{0,1\}_{\leq k}^{|Y|}\right\}\subseteq \mathbb{F}\text{-}span\left\{\sigma_Y(\partial_Y^{\leq rk}P)\right\}.$$

The following corollary can be obtained from Lemma 14. and proof of this corollary can be found in [9].

▶ Corollary 15 (Similar to Lemma 4.8 in [14]). For a multi-r-ic polynomial P(Y, Z), $\text{mSED}_{k,\ell}^{[Y,Z]}(P(Y,Z)) \leq \text{PSSPD}_{rk,\ell}^{[Y,Z]}(P(Y,Z))$.

Complexity measure for the $\Sigma \wedge \Sigma \Pi$ circuits of low bottom support

The arguments from [8] can be adapted to get the following lemma. and its proof can be found in [9].

▶ Lemma 16. Let m, k, ℓ and t be positive integers such that $\ell + kt < \frac{m}{2}$. Let Y and Z be disjoint sets of variables such that |Z| = m. Let C(Y, Z) be a depth four $\Sigma \wedge \Sigma \Pi$ circuit of bottom support at most t with respect to variables from Z, and size s. Then, $\mathrm{PSSPD}_{k,\ell}^{[Y,Z]}(C)$ is at most $s \cdot (k+1) \cdot \binom{m}{\ell+kt} \cdot (\ell+kt)$.

3 Hard Polynomial and Restrictions

In this section we recall the definition of the polynomial family and the set of deterministic and random restrictions imposed on the polynomial family, from [7].

3.1 Polynomial Family: Iterated Matrix Multiplication polynomial

Let $X^{(1)}, X^{(2)}, \ldots, X^{(d)}$ be d generic $n \times n$ matrices defined over disjoint set of variables. For any $k \in [d]$, let $x_{i,j}^{(k)}$ be the variable in the matrix $X^{(k)}$ indexed by $(i,j) \in [n] \times [n]$. The Iterated Matrix Multiplication polynomial, denoted by the family $\{\mathsf{IMM}_{n,d}\}$, is defined as follows.

$$\mathsf{IMM}_{n,d}(X) = \sum_{i_1,i_2,\dots,i_{d-1} \in [n]} x_{1,i_1}^{(1)} x_{i_1,i_2}^{(2)} \dots x_{i_{(d-2)},i_{(d-1)}}^{(d-1)} x_{i_{(d-1)},1}^{(d)}.$$

⁷ Here **e** is a |Y|-long vector that indicates the support of multilinear monomials. $Y^{\mathbf{e}}$ is a shorthand representation of $y_1^{e_1}y_2^{e_2}\dots y_{|Y|}^{e_{|Y|}}$.

3.2 Deterministic and Random Restrictions

Let k and α be a parameters such that $d=(2\alpha+3)\cdot k$. Let the d matrices be divided into k contiguous blocks of matrices B_1,B_2,\ldots,B_k such that each block B_i contains $2\alpha+3$ matrices. By suitable renaming, let us assume that each block B_i contains the following matrices.

$$X^{(i,L,\alpha+1)}, \cdots, X^{(i,L,2)}, X^{(i,L,1)}, X^{(i)}, X^{(i,R,1)}, X^{(i,R,2)}, \cdots, X^{(i,R,\alpha+1)}.$$

Let us first consider the following set of restrictions, first deterministic and then randomized.

Deterministic Restrictions

Let $V_0: X \mapsto Y_0 \sqcup Z_0 \sqcup \{0,1\}$ be a deterministic restriction of the variables X in to disjoint variable sets Y_0, Z_0 , and $\{0,1\}$ as follows. For all $i \in [k]$,

- The variables in matrix in $X^{(i)}$ are each set to a distinct Y_0 variable. Henceforth, we shall refer to this as $Y^{(i)}$ matrix.
- The entries of the first row of matrix $X^{(i,L,\alpha+1)}$ are all set to 1 and the rest of the matrix to 0.
- The entries of the first column of matrix $X^{(i,R,\alpha+1)}$ are all set to 1 and the rest of the matrix to 0.
- The rest of the variables are all set to distinct Z_0 variables. Henceforth, for all $b \in \{L, R\}$ and $j \in [\alpha]$, we shall refer to the matrix $X^{(i,b,j)}$ as $Z^{(i,b,j)}$ matrix.

Random Restrictions

Let η and ε' be two fixed constants in (0,1). Let $V_1: Y_0 \sqcup Z_0 \mapsto Y \sqcup Z \sqcup \{0,1\}$ be a random restriction of the variables $Y_0 \sqcup Z_0$ as follows.

- Matrix $Z^{(i,L,1)}$: For every column, pick n^{η} distinct elements uniformly at random and keep these elements alive. Set the other entries in this matrix to zero.
- Matrix $Z^{(i,R,1)}$: For every row, pick n^{η} distinct elements uniformly at random and keep these elements alive. Set the other entries in this matrix to zero.
- Matrices $Z^{(i,L,j)}$ for all $j \in [2, \alpha \varepsilon' \log n]$: For every column, pick 2 distinct elements uniformly at random and set all the other entries to zero.
- Matrices $Z^{(i,R,j)}$ for all $j \in [2, \alpha \varepsilon' \log n]$: For every row, pick 2 distinct elements uniformly at random and set all the other entries to zero.
- Matrices $Z^{(i,L,j)}$ for all $j > \alpha \varepsilon' \log n$: For every column, pick 1 element uniformly at random and set the other elements in that row to zero.
- Matrices $Z^{(i,R,j)}$ for all $j > \alpha \varepsilon' \log n$: For every row, pick 1 element uniformly at random and set the other elements in that row to zero.

Let D be the distribution of all the restrictions $V: X \mapsto Y \sqcup Z \sqcup \{0, 1\}$ such that $V = V_1 \circ V_0$ where V_0 and V_1 are deterministic and random restrictions respectively, as described above. Let m be used to denote the number of Z variables left after the restriction and $m = 2kn(n^{\eta} + 2(\alpha - \varepsilon' \log n - 1) + \varepsilon' \log n) = O(n^{1+\eta}k)$ when $\alpha \leq O(n^{\eta})$.

Effect of Restrictions on $IMM_{n.d}$

Let $g_{1,a}^{(i,L)}(Z)$ be the (1,a)th entry in product of matrices $\prod_{j=0}^{\alpha} X^{(i,L,\alpha+1-j)}|_V$. Let $g_{b,1}^{(i,R)}(Z)$ be the (b,1)th entry in product of matrices $\prod_{j=1}^{\alpha+1} X^{(i,R,j)}|_V$. Let $g^{(i)}$ the (1,1)th entry in the product of all the matrices in the block B_i . Then we can express $g^{(i)}$ as follows.

$$g^{(i)}(Y,Z) = \sum_{a,b \in [n]} g_{1,a}^{(i,L)}(Z) \cdot y_{a,b}^{(i)} \cdot g_{b,1}^{(i,R)}(Z).$$

Let $P|_V(Y,Z)$ obtained by restricting $\mathsf{IMM}_{n,d}(X)$ with the restriction $V \leftarrow D$. Thus,

$$P|_V(Y,Z) = \prod_{i=1}^k g^{(i)}(Y,Z).$$

To summarize, for some parameters α, k, η and $m, P|_V$ is polynomial in $\mathbb{F}[Y \sqcup Z]$ such that its degree is $d = (2\alpha + 3) \cdot k$, and has $m = O(n^{1+\eta}k)$ many Z variables. Here the definition of the polynomial $P|_V$ is heavily dependent on $V \leftarrow D$ and the choice of parameters α, k, ε' and η .

Effect on random restrictions

▶ Lemma 17 (Lemma 8, [7]). Let t be a parameter. Let C be any depth four circuit of size at most $s
leq n^{\frac{t}{2}}$ that computes $\mathsf{IMM}_{n,d}$. Then with a probability of at least 1 - o(1), over $V \leftarrow D$ (where $V : X \mapsto Y \sqcup Z \sqcup \{0,1\}$), $C|_V$ is a depth four circuit of bottom support at most t in Z variables that computes the polynomial $P|_V(Y,Z)$.

3.3 Complexity of $P|_V$

Choice of parameters

We borrow the setting of the parameters involved directly from $[7]^8$.

$$\begin{array}{lll} \bullet & \varepsilon' = 0.34, & \bullet & d = (2\alpha + 3)k, \\ \bullet & \eta = 0.05, & \bullet & m = \Theta(n^{1+\eta}k) = \Theta(n^{1.05}k), \\ \bullet & \varepsilon = \varepsilon' - \eta = 0.29, & \bullet & \ell = \frac{m}{2}(1 - \Gamma), \\ \bullet & \omega(\log n) \leq d \leq n^{0.01}, & \bullet & (1 + \Gamma)^{\alpha} = 2n^{\varepsilon} \text{ such that } \Gamma = O_{\varepsilon}\left(\frac{\ln n}{\alpha}\right), \end{array}$$

We shall now recall the following from [7].

▶ **Theorem 18** (Discussion above Theorem 17, [7]). Let n be a large enough integer. Let $m, d, \ell, \alpha, k, \varepsilon$ and τ be as described above.

$$PSSPD_{k,\ell}^{[Y,Z]}(P|_V) \ge \frac{\left(\frac{m}{m-\ell}\right)^{2\alpha k} \cdot {m-2\alpha k \choose \ell}}{2^{O(k)} \cdot \left(\frac{\ell}{m-\ell}\right)^{2\alpha k(1-\tau)}}.$$

⁸ In an attempt to have a clean up the notation in comparison to [7], we make the following notational changes – the parameter α here corresponds to k' in [7], the parameter k here corresponds to r' in [7]. Further the parameter k = d - 3r' = 2k'r' in [7] translates to $2\alpha k$ here. The rest of the parameters $\varepsilon, \varepsilon', \eta$ and τ are the same in both the papers.

Note that for a N-variate polynomial P(X,Y), the measure in [7] was defined to be equal to dim (\mathbb{F} -span {mult₀ ($Z^{=\ell} \cdot \sigma_Y \left(\partial_Y^{=k} P \right)$)}) where $\mathrm{mult_0}(P) = P \mod \left\{ x_i^2 \mid i \in [N] \right\}$ compared to the measure here which is equal to dim (\mathbb{F} -span {mult ($Z^{=\ell} \cdot \sigma_Y \left(\partial_Y^{\leq k} P \right)$)}) where $\mathrm{mult}(P) = P \mod \left\{ x_i^2 - x_i \mid i \in [N] \right\}$. This change of definition would not affect the bound as the lower bound in [7] counts the leading monomials of support size and degree both equal to $d - k + \ell$, and $\sigma_Y(\partial^{\leq k} P|_V) = \emptyset$ for the polynomial $P|_V$ described above.

4 Functional Lower Bounds against restricted $\Sigma \wedge \Sigma \Pi$ Circuits

As mentioned in the proof overview, we first prove a lower bound against bounded bottom support depth four circuits and then escalate this lower bound to circuits without the restriction on bottom support.

▶ **Lemma 19.** Let n and d be large integers such that $\omega(\log^2 n) \leq d \leq n^{0.01}$. Let α, k, r and t be parameters such that $d = (2\alpha + 3)k$ and $r \leq \frac{\alpha}{200t}$. Any depth four $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree r and bounded bottom fan-in at most t, computing a function equivalent to $P|_V(X_V)$ (for $V \leftarrow D$) on $\{0,1\}^{|X_V|}$, must have size at least $n^{\Omega(k)}$.

Proof of this lemma can be found in [9]. Using this lemma, we shall prove Theorem 3.

Proof of Theorem 3

For a large integer n, let d be such that $\omega(\log^2 n) \leq d \leq n^{0.01}$. Let t be a parameter that we shall soon fix. Let C be a $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most r, and size $s \leq n^{\frac{t}{2}}$ that computes a polynomial Q(X) that is functionally equivalent to $\mathsf{IMM}_{n,d}(X)$ (over $\{0,1\}^{n^2d}$). Let α and k be parameters such that $d=(2\alpha+3)k$. Recall that a restriction $V \leftarrow D$ fixes a subset of variables to values in $\{0,1\}$ and maps the rest to distinct Y and Z variables. For any such restriction $V \leftarrow D$, let $X_V = Y \sqcup Z$ be the set of variables in X that are not set to values in $\{0,1\}$ by V. From Lemma 17 we know that with a probability of at least 1-o(1), the circuit C_V obtained by applying the restriction V to C is a $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most r, size s and bottom support at most s. Let s0 be the polynomial computed by s1 cover s2 variables. We shall now show that s3 functionally equivalent to s4 over s4 variables. We shall now show that s5 functionally equivalent to s6 variables.

Let the set $S_V \subset \{0,1\}^{n^2d}$ be the subset of points such that for all $\mathbf{a} \in S_V$, if $x_i \in X \setminus X_V$ and V sets x_i to $b \in \{0,1\}$, then the value at the i'th location of \mathbf{a} , $\mathbf{a}_i = b$. Since Q(X) and $\mathsf{IMM}_{n,d}(X)$ are functionally equivalent over all of $\{0,1\}^{n^2d}$, they are functionally equivalent over S_V as well. Thus, $Q_V(\mathbf{a}|_{X_V}) = Q(\mathbf{a}) = \mathsf{IMM}(\mathbf{a}) = P|_V(\mathbf{a}|_{X_V})$ for all $\mathbf{a} \in S_V$. Here $\mathbf{a}|_{X_V} \in \{0,1\}^{|X_V|}$ corresponds to projection of $\mathbf{a} \in \{0,1\}^{n^2d}$ to locations corresponding to the variables in X_V .

This implies that $Q_V(X_V)$ and $P|_V(X_V)$ are functionally equivalent over $\{0,1\}^{|X_V|}$ and thus, there is a $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most r, size $s \leq n^{\frac{t}{2}}$ and bottom support at most t that functionally computes $P|_V(Y,Z)$. On the other hand if r is at most $\frac{\alpha}{200t}$ then from Lemma 19 we know that any $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree at most r and bottom support at most t that functionally computes $P|_V$ must have size $n^{\Omega(k)}$. Putting these together by fixing the value of t to 3k we get that s must at least be $n^{\Omega(k)}$. Since r is at most $\frac{\alpha}{200t}$, under this substitution of t, this value computes to $\frac{1}{200 \cdot 3k} \cdot \left(\frac{d}{2k} - \frac{3}{2}\right) = \frac{d}{1200k^2} - \frac{1}{400k}$.

References

Manindra Agrawal and V. Vinay. Arithmetic circuits: A chasm at depth four. In 49th Annual IEEE Symposium on Foundations of Computer Science, FOCS 2008, October 25-28, 2008, Philadelphia, PA, USA, pages 67-75. IEEE Computer Society, 2008. doi:10.1109/FOCS.2008. 32.

- 2 Eric Allender and Vivek Gore. A uniform circuit lower bound for the permanent. SIAM J. Comput., 23(5):1026–1049, 1994. doi:10.1137/S0097539792233907.
- Wikraman Arvind, Johannes Köbler, Uwe Schöning, and Rainer Schuler. If NP has polynomial-size circuits, then MA=AM. *Theor. Comput. Sci.*, 137(2):279–282, 1995. doi:10.1016/0304-3975(95)91133-B.
- Walter Baur and Volker Strassen. The complexity of partial derivatives. *Theor. Comput. Sci.*, 22:317–330, 1983. doi:10.1016/0304-3975(83)90110-X.
- 5 Richard Beigel and Jun Tarui. On ACC. Comput. Complex., 4:350–366, 1994. doi:10.1007/BF01263423.
- Peter Bürgisser. Cook's versus valiant's hypothesis. Theoretical Computer Science, 235(1):71 88, 2000. URL: http://www.sciencedirect.com/science/article/pii/S0304397599001838, doi:https://doi.org/10.1016/S0304-3975(99)00183-8.
- 7 Suryajith Chillara. New exponential size lower bounds against depth four circuits of bounded individual degree. Electronic Colloquium on Computational Complexity (ECCC), 27:33, 2020. URL: https://eccc.weizmann.ac.il/report/2020/033.
- 8 Suryajith Chillara. On Computing Multilinear Polynomials Using Multi-r-ic Depth Four Circuits. In Christophe Paul and Markus Bläser, editors, 37th International Symposium on Theoretical Aspects of Computer Science (STACS 2020), volume 154 of Leibniz International Proceedings in Informatics (LIPIcs), pages 47:1–47:16, Dagstuhl, Germany, 2020. Schloss Dagstuhl-Leibniz-Zentrum fuer Informatik. doi:10.4230/LIPIcs.STACS.2020.47.
- 9 Suryajith Chillara. Functional lower bounds for restricted arithmetic circuits of depth four. Electron. Colloquium Comput. Complex., page 105, 2021. URL: https://eccc.weizmann.ac.il/report/2021/105.
- Suryajith Chillara, Christian Engels, Nutan Limaye, and Srikanth Srinivasan. A near-optimal depth-hierarchy theorem for small-depth multilinear circuits. In Mikkel Thorup, editor, 59th IEEE Annual Symposium on Foundations of Computer Science, FOCS 2018, Paris, France, October 7-9, 2018, pages 934–945. IEEE Computer Society, 2018. doi:10.1109/FOCS.2018.00092
- Suryajith Chillara, Nutan Limaye, and Srikanth Srinivasan. Small-depth multilinear formula lower bounds for iterated matrix multiplication with applications. SIAM J. Comput., 48(1):70–92, 2019. doi:10.1137/18M1191567.
- Suryajith Chillara and Partha Mukhopadhyay. Depth-4 lower bounds, determinantal complexity: A unified approach. computational complexity, May 2019. doi:10.1007/s00037-019-00185-4.
- Ismor Fischer. Sums of like powers of multivariate linear forms. *Mathematics Magazine*, 67(1):59–61, 1994. doi:10.1080/0025570X.1994.11996185.
- Michael A. Forbes, Mrinal Kumar, and Ramprasad Saptharishi. Functional lower bounds for arithmetic circuits and connections to boolean circuit complexity. In Ran Raz, editor, 31st Conference on Computational Complexity, CCC 2016, May 29 to June 1, 2016, Tokyo, Japan, volume 50 of LIPIcs, pages 33:1–33:19. Schloss Dagstuhl Leibniz-Zentrum für Informatik, 2016. doi:10.4230/LIPIcs.CCC.2016.33.
- Hervé Fournier, Nutan Limaye, Guillaume Malod, and Srikanth Srinivasan. Lower bounds for depth-4 formulas computing iterated matrix multiplication. SIAM J. Comput., 44(5):1173–1201, 2015. doi:10.1137/140990280.
- Dima Grigoriev and Alexander A. Razborov. Exponential lower bounds for depth 3 arithmetic circuits in algebras of functions over finite fields. *Appl. Algebra Eng. Commun. Comput.*, 10(6):465–487, 2000. doi:10.1007/s002009900021.

- 17 Ankit Gupta, Pritish Kamath, Neeraj Kayal, and Ramprasad Saptharishi. Approaching the chasm at depth four. Journal of the ACM (JACM), 61(6):33, 2014. doi:10.1145/2629541.
- Ankit Gupta, Pritish Kamath, Neeraj Kayal, and Ramprasad Saptharishi. Arithmetic circuits: A chasm at depth 3. SIAM Journal of Computing, 45(3):1064–1079, 2016. doi:10.1137/140957123.
- Nikhil Gupta, Chandan Saha, and Bhargav Thankey. A super-quadratic lower bound for depth four arithmetic circuits. In Shubhangi Saraf, editor, 35th Computational Complexity Conference, CCC 2020, July 28-31, 2020, Saarbrücken, Germany (Virtual Conference), volume 169 of LIPIcs, pages 23:1-23:31. Schloss Dagstuhl Leibniz-Zentrum für Informatik, 2020. doi:10.4230/LIPIcs.CCC.2020.23.
- 20 Richard M. Karp and Richard J. Lipton. Some connections between nonuniform and uniform complexity classes. In *Proceedings of the Twelfth Annual ACM Symposium on Theory of Computing*, STOC '80, pages 302–309, New York, NY, USA, 1980. Association for Computing Machinery. doi:10.1145/800141.804678.
- Neeraj Kayal, Nutan Limaye, Chandan Saha, and Srikanth Srinivasan. Super-polynomial lower bounds for depth-4 homogeneous arithmetic formulas. In David B. Shmoys, editor, Symposium on Theory of Computing, STOC 2014, New York, NY, USA, May 31 June 03, 2014, pages 119–127. ACM, 2014. doi:10.1145/2591796.2591823.
- Neeraj Kayal, Nutan Limaye, Chandan Saha, and Srikanth Srinivasan. An exponential lower bound for homogeneous depth four arithmetic formulas. SIAM J. Comput., 46(1):307–335, 2017. doi:10.1137/151002423.
- 23 Neeraj Kayal and Chandan Saha. Multi-k-ic depth three circuit lower bound. *Theory Comput. Syst.*, 61(4):1237–1251, 2017. doi:10.1007/s00224-016-9742-9.
- Neeraj Kayal, Chandan Saha, and Ramprasad Saptharishi. A super-polynomial lower bound for regular arithmetic formulas. In David B. Shmoys, editor, Symposium on Theory of Computing, STOC 2014, New York, NY, USA, May 31 June 03, 2014, pages 146–153. ACM, 2014. doi:10.1145/2591796.2591847.
- Neeraj Kayal, Chandan Saha, and Sébastien Tavenas. On the size of homogeneous and of depth-four formulas with low individual degree. *Theory of Computing*, 14(16):1–46, 2018. doi:10.4086/toc.2018.v014a016.
- Mrinal Kumar and Shubhangi Saraf. Superpolynomial lower bounds for general homogeneous depth 4 arithmetic circuits. In Javier Esparza, Pierre Fraigniaud, Thore Husfeldt, and Elias Koutsoupias, editors, Automata, Languages, and Programming 41st International Colloquium, ICALP 2014, Copenhagen, Denmark, July 8-11, 2014, Proceedings, Part I, volume 8572 of Lecture Notes in Computer Science, pages 751–762. Springer, 2014. doi: 10.1007/978-3-662-43948-7_62.
- Mrinal Kumar and Shubhangi Saraf. The limits of depth reduction for arithmetic formulas: It's all about the top fan-in. SIAM J. Comput., 44(6):1601–1625, 2015. doi:10.1137/140999220.
- Mrinal Kumar and Shubhangi Saraf. On the power of homogeneous depth 4 arithmetic circuits. SIAM J. Comput., 46(1):336–387, 2017. doi:10.1137/140999335.
- Nutan Limaye, Srikanth Srinivasan, and Sébastien Tavenas. Superpolynomial lower bounds against low-depth algebraic circuits. *Electron. Colloquium Comput. Complex.*, 28:81, 2021. URL: https://eccc.weizmann.ac.il/report/2021/081.
- 30 Cody D. Murray and R. Ryan Williams. Circuit lower bounds for nondeterministic quasi-polytime from a new easy witness lemma. SIAM J. Comput., 49(5), 2020. doi:10.1137/18M1195887.
- Noam Nisan and Avi Wigderson. Lower bounds on arithmetic circuits via partial derivatives. Computational Complexity, 6(3):217–234, 1997. doi:10.1007/BF01294256.
- Ran Raz. Multilinear-NC² ≠ multilinear-NC¹. In proceedings of Foundations of Computer Science (FOCS), pages 344–351, 2004. doi:10.1109/FOCS.2004.42.
- Ran Raz. Separation of multilinear circuit and formula size. *Theory of Computing*, 2(1):121–135, 2006. doi:10.4086/toc.2006.v002a006.

Ran Raz. Elusive functions and lower bounds for arithmetic circuits. *Theory Comput.*, 6(1):135-177, 2010. doi:10.4086/toc.2010.v006a007.

- Ran Raz and Amir Yehudayoff. Balancing syntactically multilinear arithmetic circuits. *Computational Complexity*, 17(4):515–535, 2008. doi:10.1007/s00037-008-0254-0.
- Ran Raz and Amir Yehudayoff. Lower bounds and separations for constant depth multilinear circuits. *Computational Complexity*, 18(2):171–207, 2009. doi:10.1007/s00037-009-0270-8.
- Herbert John Ryser. *Combinatorial mathematics*, volume 14. American Mathematical Soc., 1963. URL: https://www.jstor.org/stable/10.4169/j.ctt5hh8v6.
- Ramprasad Saptharishi. A survey of lower bounds in arithmetic circuit complexity version 8.0.4. Github survey, 2019. URL: https://github.com/dasarpmar/lowerbounds-survey/releases/.
- 39 Victor Shoup and Roman Smolensky. Lower bounds for polynomial evaluation and interpolation problems. Computational Complexity, 6(4):301–311, 1997. doi:10.1007/BF01270384.
- 40 Amir Shpilka and Amir Yehudayoff. Arithmetic circuits: A survey of recent results and open questions. Found. Trends Theor. Comput. Sci., 5(3-4):207–388, 2010. doi:10.1561/0400000039.
- 41 Leslie G. Valiant. Completeness classes in algebra. In Michael J. Fischer, Richard A. DeMillo, Nancy A. Lynch, Walter A. Burkhard, and Alfred V. Aho, editors, Proceedings of the 11h Annual ACM Symposium on Theory of Computing, April 30 May 2, 1979, Atlanta, Georgia, USA, pages 249–261. ACM, 1979. doi:10.1145/800135.804419.
- 42 Leslie G. Valiant. Why is Boolean Complexity Theory so Difficult?, pages 84–94. London Mathematical Society Lecture Note Series. Cambridge University Press, 1992. doi:10.1017/CB09780511526633.008.
- V. Vinay. Counting auxiliary pushdown automata and semi-unbounded arithmetic circuits. In *Proceedings of the Sixth Annual Structure in Complexity Theory Conference, Chicago, Illinois, USA, June 30 July 3, 1991*, pages 270–284. IEEE Computer Society, 1991. doi: 10.1109/SCT.1991.160269.
- 44 Ryan Williams. Nonuniform ACC circuit lower bounds. J. ACM, 61(1):2:1-2:32, 2014. doi:10.1145/2559903.